

FIGURE 1

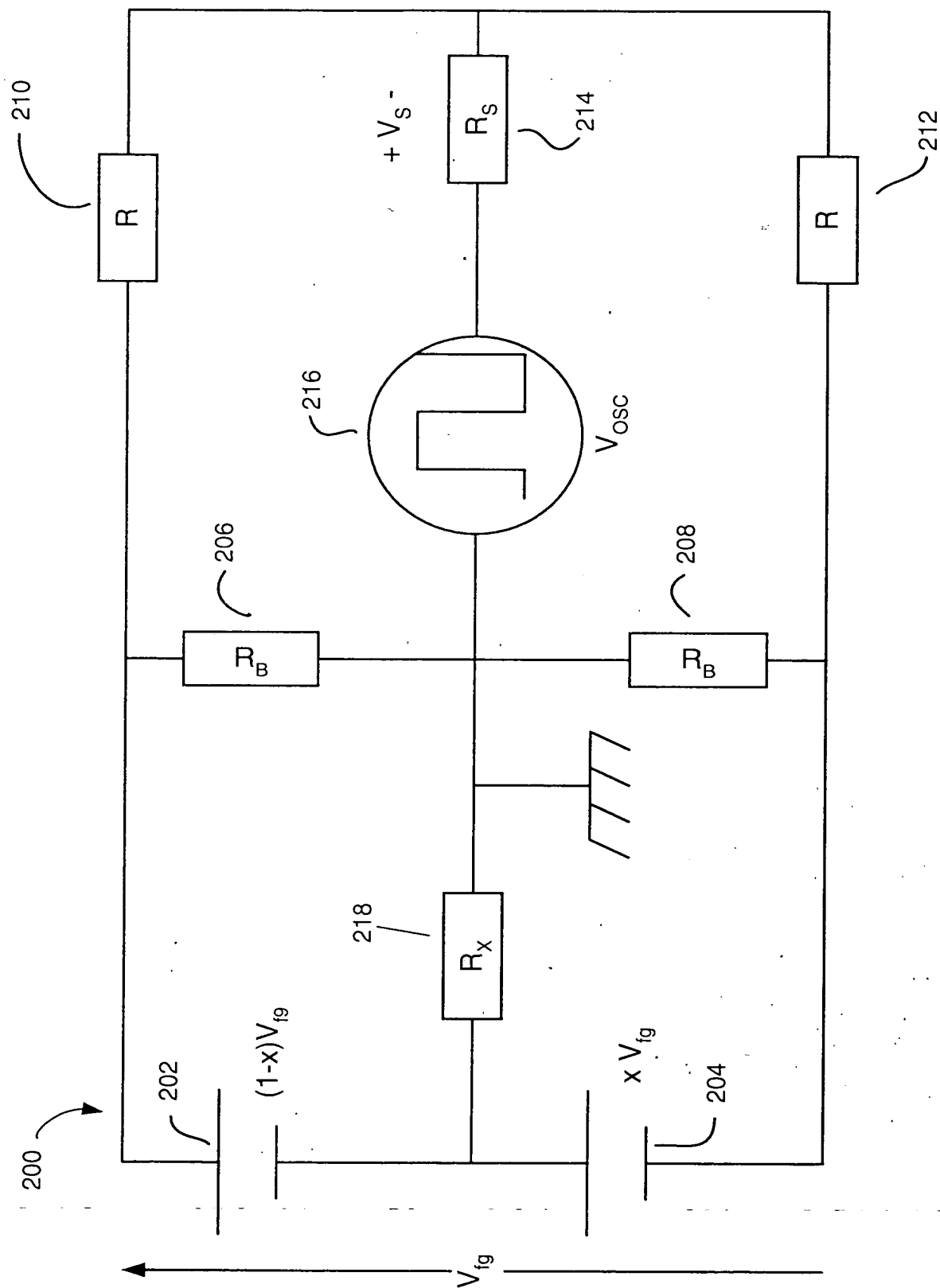


FIGURE 2

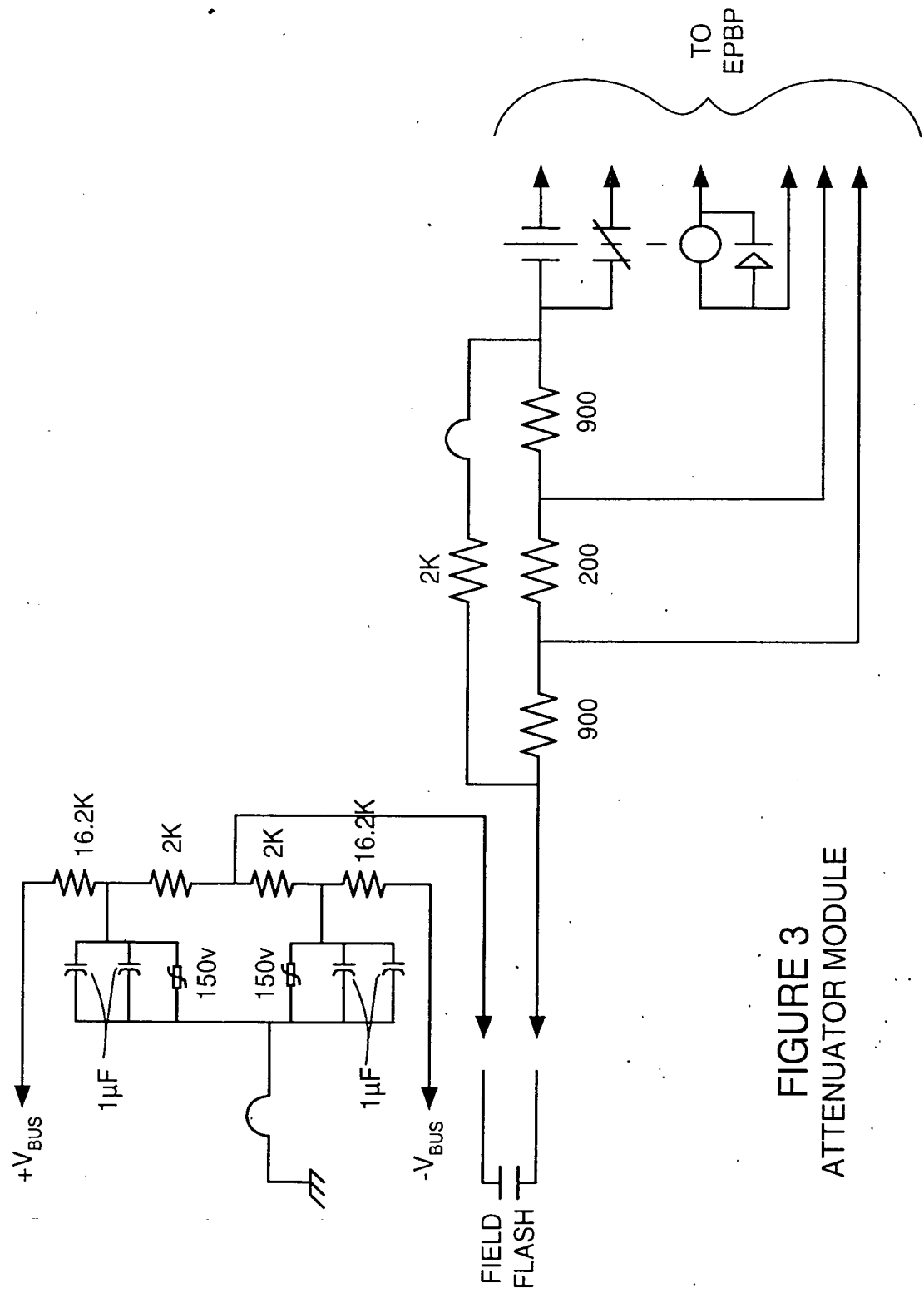


FIGURE 3
ATTENUATOR MODULE

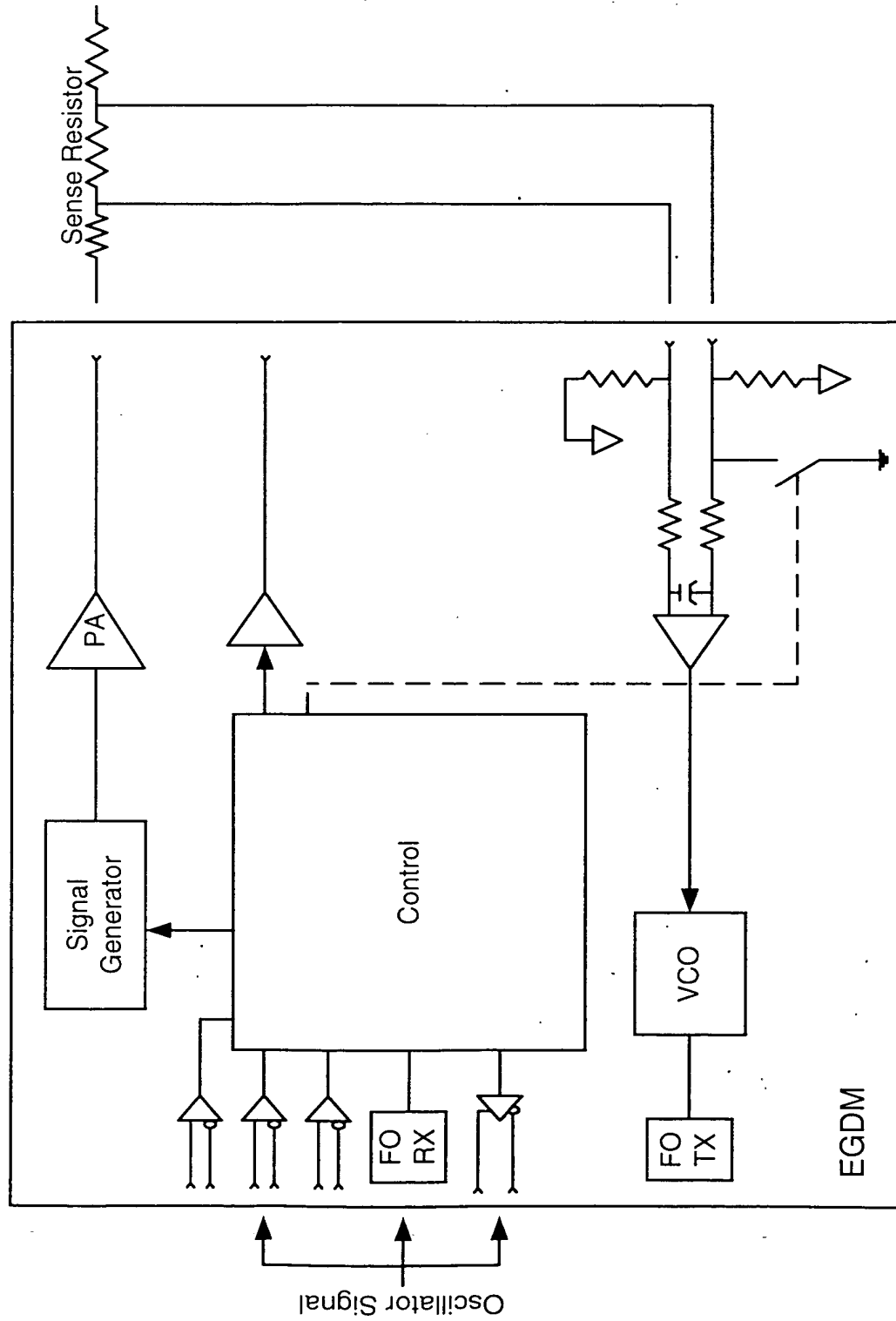


FIGURE 4

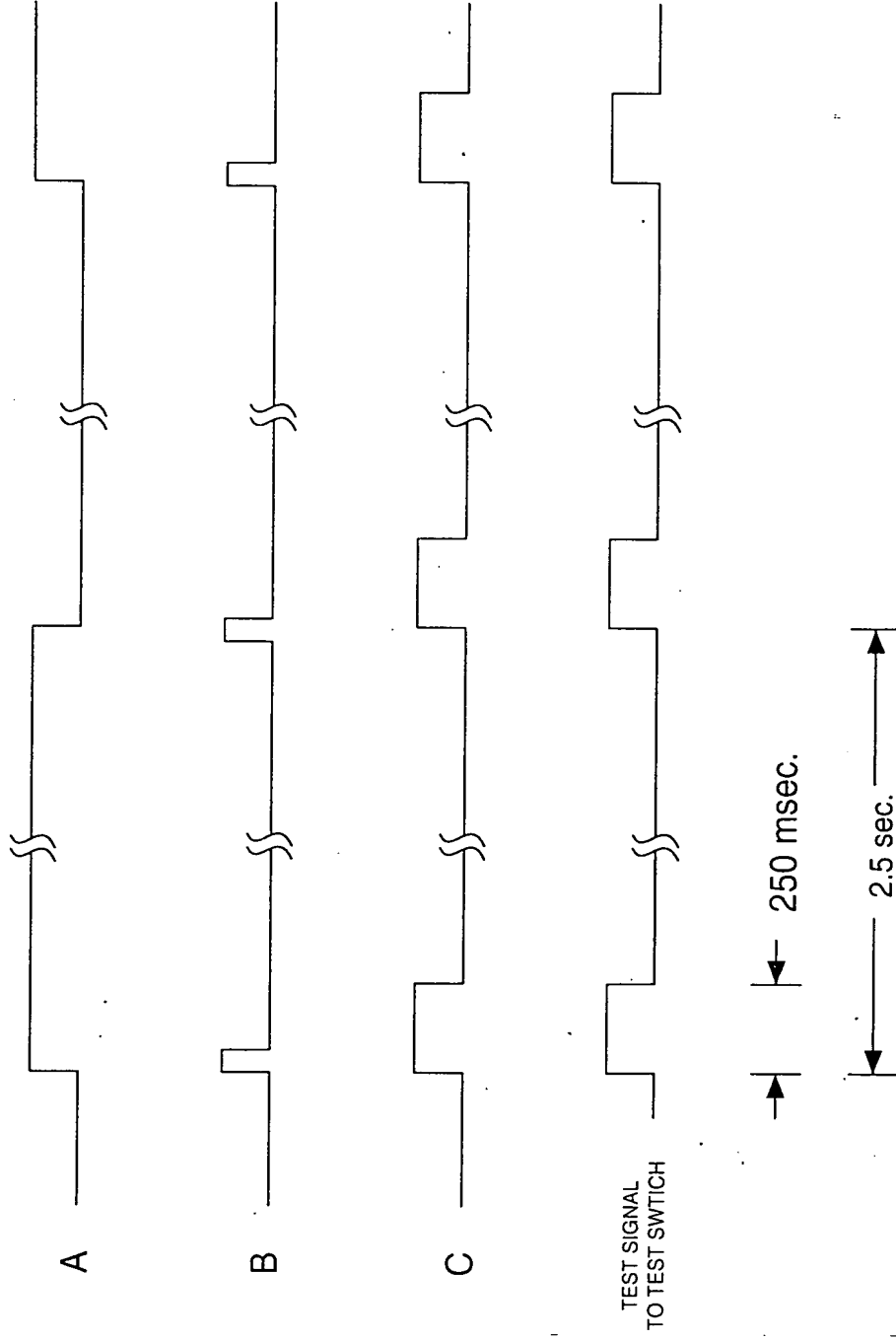


FIGURE 6
TIMING DIAGRAM

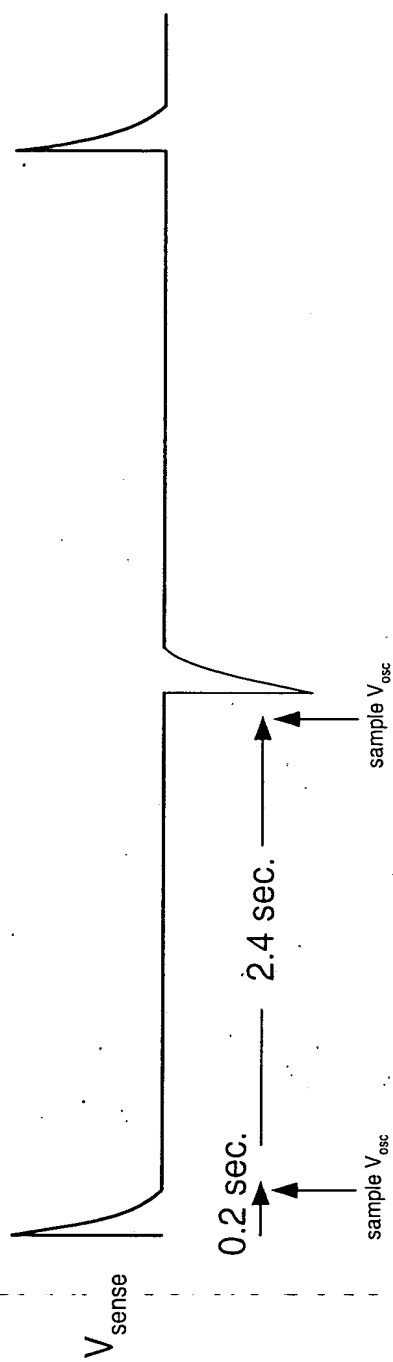
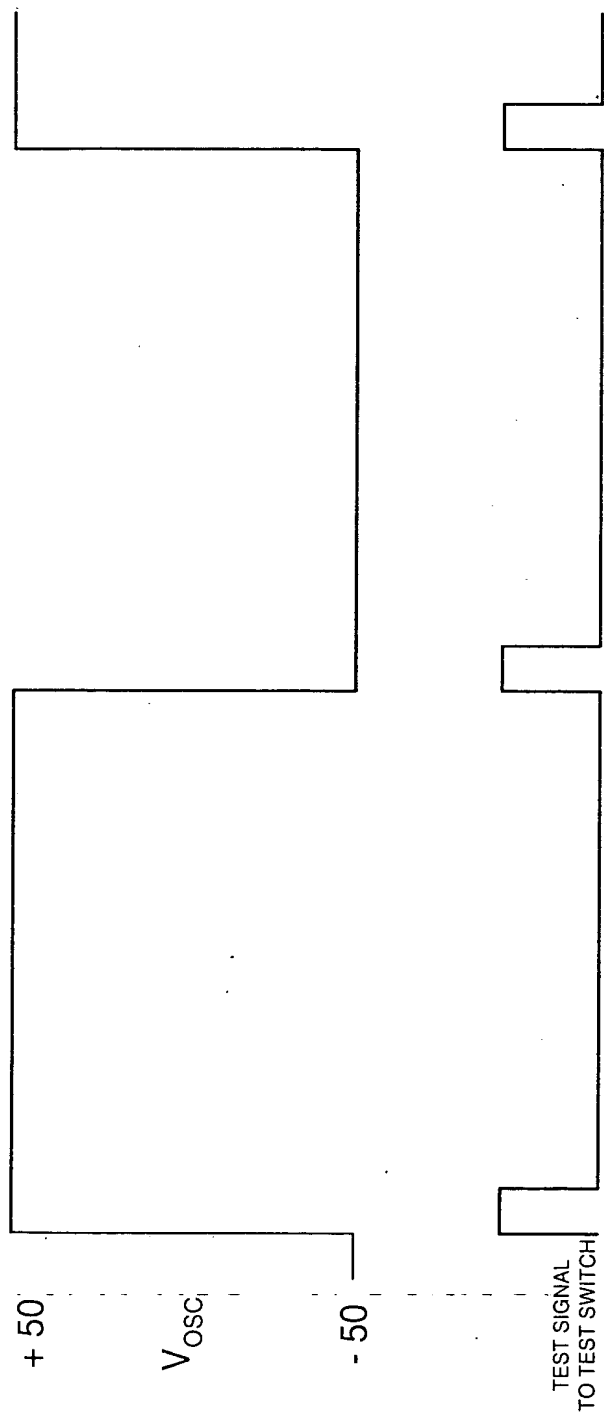


FIGURE 7

FIGURE 8
TIMING DIAGRAM FOR TEST MODE OPERATION

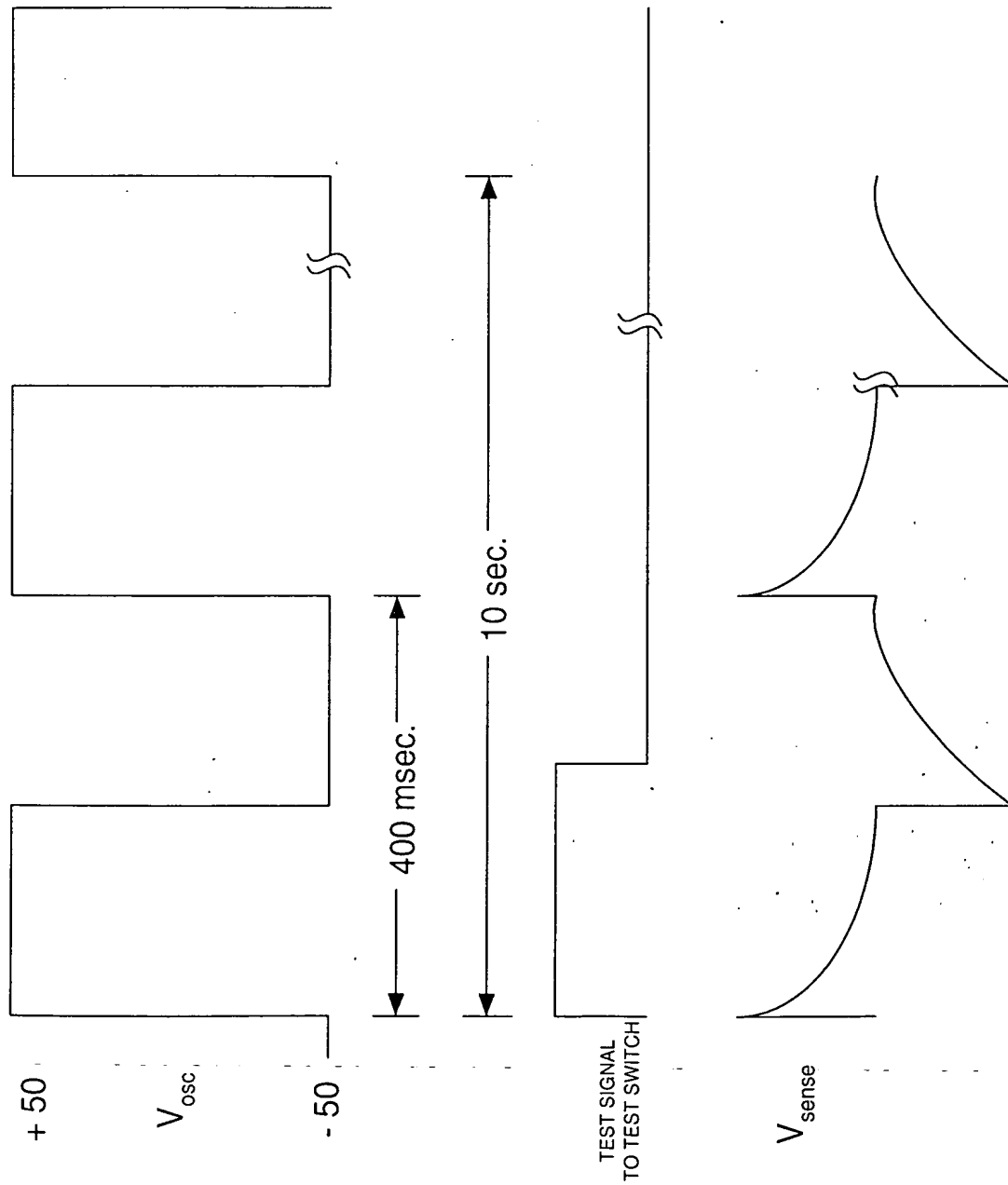


FIGURE 9
OPERATION OF FIELD GROUND DETECTOR DURING TEST MODE

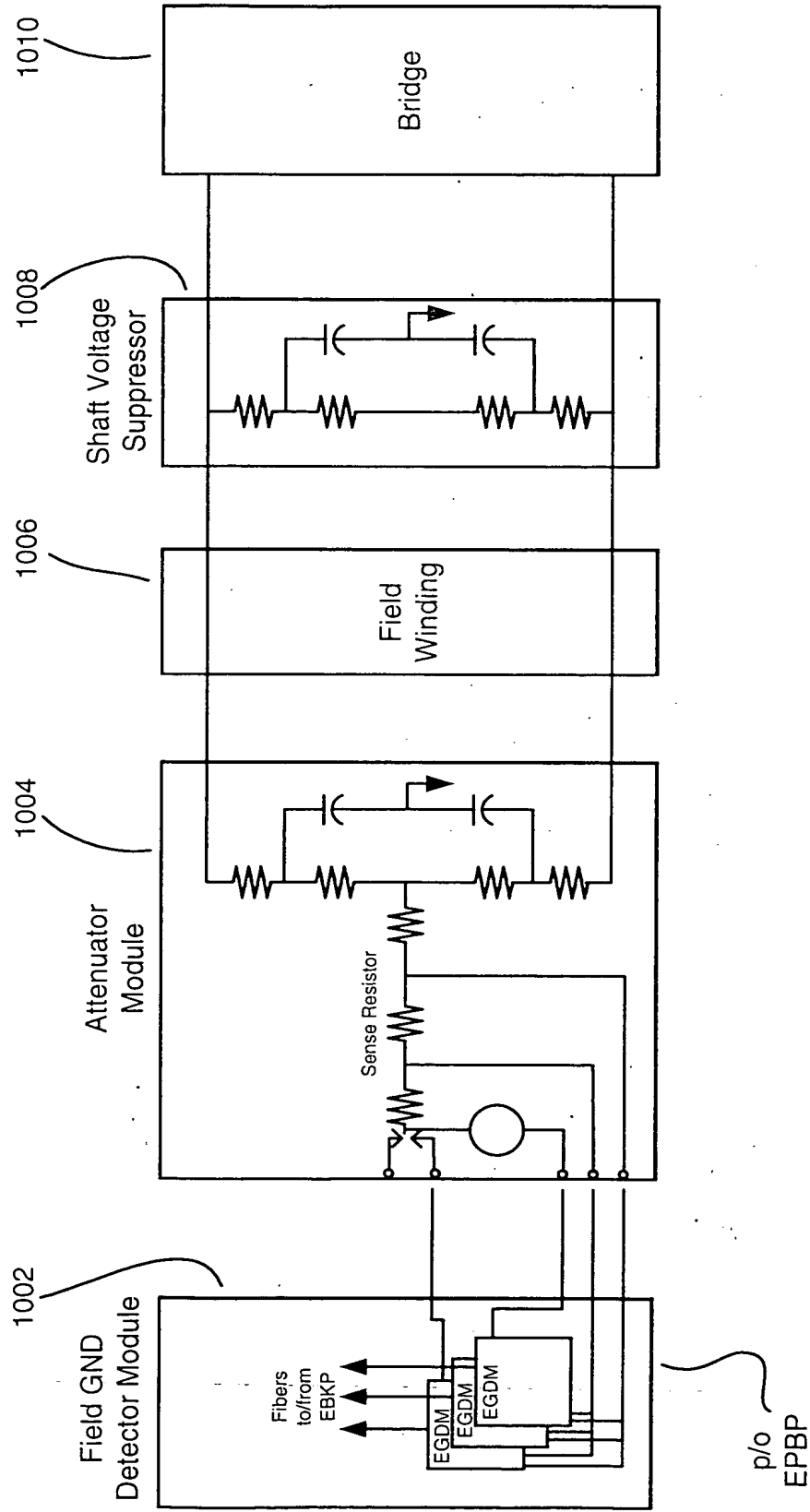


FIGURE 10

The diagram illustrates the architecture of the EPBP system, consisting of three parallel processing channels (M1, M2, and C) and an attention module.

Processing Channels (M1, M2, C):

- Inputs:** Each channel receives an "Osc. Sig." (Oscillator Signal) from its respective module (M1, M2, or C) and a "VCO Sig." (Variable Control Oscillator Signal) from the same module.
- Internal Components:** Each channel contains a "Control" block, a "Signal Generator", a "PA" (Power Amplifier), a "FO RX" (Frequency Offset Receiver), a "FO TX" (Frequency Offset Transmitter), and a "VCO" (Variable Control Oscillator).
- Connections:** The "Control" block is connected to the "Signal Generator" and the "PA". The "FO RX" and "FO TX" are connected to the "VCO". The "VCO" is connected to the "Control" block.
- Outputs:** The "PA" outputs a signal to the "Sense Resist." (Sense Resistor) in the "Atten. Module". The "VCO" outputs a signal to the "Atten. Module".

Atten. Module (Attention Module):

- Components:** The module contains a "Sense Resist." (Sense Resistor) and an "Atten. Module" (Attention Module).
- Connections:** The "Sense Resist." is connected to the "PA" outputs of all three channels. The "Atten. Module" is connected to the "VCO" outputs of all three channels.

Legend:

- PA:** Power Amplifier
- FO RX:** Frequency Offset Receiver
- FO TX:** Frequency Offset Transmitter
- VCO:** Variable Control Oscillator
- Control:** Control block
- Signal Generator:** Signal Generator
- Sense Resist.:** Sense Resistor
- Atten. Module:** Attention Module

Figure 1

p/o EPBP

SECRET

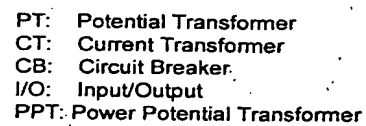


Figure 12